



**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q63926

Koutaro HACHIYA

Appln. No.: 09/822,512

Group Art Unit: 2128

Confirmation No.: 7509

Examiner: Fred Ferris

Filed: April 2, 2001

For: METHOD AND APPARATUS FOR MATRIX REORDERING AND ELECTRONIC  
CIRCUIT SIMULATION

**PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. § 1.136**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136, Applicant hereby petitions for an extension of time of one months, extending the time for responding to the Office Action of April 19, 2005 to August 19, 2005.

A check for the statutory fee of \$120.00 is attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this sheet is enclosed.

08/19/2005 EFLORES 00000062 09822512

01 FC:1251

120.00 0P

SUGHRUE MION, PLLC  
Telephone: (202) 293-7060  
Facsimile: (202) 293-7860

WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Respectfully submitted,

Chid S. Iyer  
Registration No. 43,355

Date: August 17, 2005